

**ENS 203 CIRCUITS I** Meriç Özcan **SEPTEMBER 2008**

[illegible]

# ENS 203 CIRCUITS I Meriç Özcan OCTOBER 2008

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		HOLIDAY																																																																																															
5 PREPARE FOR THE LAB INTRO THIS WEEK →	6 LAB INTRO	7 CHAPTER 2 2.1—2.3 Resistive circuits, parallel and series networks...	8	9 CHAPTER 2 2.4—2.5 Node Voltage Analysis, Mesh Current Analysis	10	11																																																																																											
12 PREPARE FOR THE LAB 1 THIS WEEK →	13 LAB 1 Instruments	14 CHAPTER 2 2.5—2.6 Mesh Current Analysis continued..	15	16 CHAPTER 2 2.6—2.8 Thevenin & Norton Circuits, Superposition...	17	18																																																																																											
19	20	21 CHAPTER 3 3.1—3.4 Capacitance and Inductance	22	23 CHAPTER 4 3.5 — 4.1 Introduction to first order RC circuits	24	25																																																																																											
26 PREPARE FOR THE LAB 2 THIS WEEK →	27 LAB 2 Thevenin Equivalence...	28 CHAPTER 4 4.1—4.2 Transient Circuits First order RC, RL circuits...	29 HOLIDAY	30 CHAPTER 4 4.3—4.5 RC, RL circuits with sources, 2nd order circuits	31																																																																																												
		<b>September 2008</b> <table> <tr><td>S</td><td>M</td><td>T</td><td>W</td><td>Th</td><td>F</td><td>Sa</td></tr> <tr><td></td><td>1</td><td>2</td><td>3</td><td>4</td><td>5</td><td>6</td></tr> <tr><td>7</td><td>8</td><td>9</td><td>10</td><td>11</td><td>12</td><td>13</td></tr> <tr><td>14</td><td>15</td><td>16</td><td>17</td><td>18</td><td>19</td><td>20</td></tr> <tr><td>21</td><td>22</td><td>23</td><td>24</td><td>25</td><td>26</td><td>27</td></tr> <tr><td>28</td><td>29</td><td>30</td><td></td><td></td><td></td><td></td></tr> </table>		S	M	T	W	Th	F	Sa		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30					<b>November 2008</b> <table> <tr><td>S</td><td>M</td><td>T</td><td>W</td><td>Th</td><td>F</td><td>Sa</td></tr> <tr><td></td><td></td><td></td><td></td><td></td><td></td><td>1</td></tr> <tr><td>2</td><td>3</td><td>4</td><td>5</td><td>6</td><td>7</td><td>8</td></tr> <tr><td>9</td><td>10</td><td>11</td><td>12</td><td>13</td><td>14</td><td>15</td></tr> <tr><td>16</td><td>17</td><td>18</td><td>19</td><td>20</td><td>21</td><td>22</td></tr> <tr><td>23</td><td>24</td><td>25</td><td>26</td><td>27</td><td>28</td><td>29</td></tr> <tr><td>30</td><td></td><td></td><td></td><td></td><td></td><td></td></tr> </table>		S	M	T	W	Th	F	Sa							1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30							Notes: <b>Wed section of the Lab for Oct 29th will be done next week.</b>
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2	3	4 CHAPTER 5 5.1—5.2 Steady State Sinusoidal Analysis, Phasors	5 LAB 2 for Wednesday section	6 CHAPTER 5 5.3—5.5 Complex Impedances, Circuit analysis, power in AC circuits	7	8																																																																																				
9 PREPARE FOR THE LAB 3 THIS WEEK	10 LAB 3 RC and RL	11 CHAPTER 5 5.6 Thevenin and Norton Equivalent Circuits...	12	13 CHAPTER 6 6.1 — 6.4 Frequency Response, First order filters, Bode Plots	14	15																																																																																				
16	17	18 CHAPTER 6 6.4 — 6.5 Bode Plots, High Pass Filters	19	20 CHAPTER 6 6.6 — 6.7 Series Resonance Parallel Resonance	21	22																																																																																				
23	24	25 REVIEW	26	27 CHAPTER 11 11.1 — 11.3 Basic Amplifier Concepts, Cascaded Amplifiers	28	29 MIDTERM																																																																																				
30		<b>October 2008</b> <table> <tr><th>S</th><th>M</th><th>T</th><th>W</th><th>Th</th><th>F</th><th>Sa</th></tr> <tr><td></td><td></td><td></td><td>1</td><td>2</td><td>3</td><td>4</td></tr> <tr><td>5</td><td>6</td><td>7</td><td>8</td><td>9</td><td>10</td><td>11</td></tr> <tr><td>12</td><td>13</td><td>14</td><td>15</td><td>16</td><td>17</td><td>18</td></tr> <tr><td>19</td><td>20</td><td>21</td><td>22</td><td>23</td><td>24</td><td>25</td></tr> <tr><td>26</td><td>27</td><td>28</td><td>29</td><td>30</td><td>31</td><td></td></tr> </table>		S	M	T	W	Th	F	Sa				1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31		<b>December 2008</b> <table> <tr><th>S</th><th>M</th><th>T</th><th>W</th><th>Th</th><th>F</th><th>Sa</th></tr> <tr><td></td><td>1</td><td>2</td><td>3</td><td>4</td><td>5</td><td>6</td></tr> <tr><td>7</td><td>8</td><td>9</td><td>10</td><td>11</td><td>12</td><td>13</td></tr> <tr><td>14</td><td>15</td><td>16</td><td>17</td><td>18</td><td>19</td><td>20</td></tr> <tr><td>21</td><td>22</td><td>23</td><td>24</td><td>25</td><td>26</td><td>27</td></tr> <tr><td>28</td><td>29</td><td>30</td><td>31</td><td></td><td></td><td></td></tr> </table>		S	M	T	W	Th	F	Sa		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31				Notes:
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<div>PREPARE FOR THE LAB 4 THIS WEEK</div>	1 <div>LAB 4 Resonance</div>	2 CHAPTER 11 & 14 11.11 Differential Amplifiers, 14.1-14.4 Operational Amplifiers	3	4 CHAPTER 14 Op-Amps continued...	5	6																																																																																																
7	8	9	10	11	12	13																																																																																																
	HOLIDAY SEMESTER BREAK																																																																																																					
14 <div>PREPARE FOR THE LAB 5 THIS WEEK</div>	15 <div>LAB 5 Amplifiers</div>	16 CHAPTER 14 Op-Amps continued... Example circuits...	17	18 CHAPTER 14 14.5—14.6 Op-Amps continued	19	20																																																																																																
21 <div>PREPARE FOR THE LAB 6 THIS WEEK</div>	22 <div>LAB 6 Radio</div>	23 CHAPTER 10 10.1—10.3 Basic Diode, Load Line Analysis, Zener Diode	24	25 CHAPTER 10 10.4—10.5 Diode Models	26	27																																																																																																
28	29	30 CHAPTER 10 10.6—10.8 Rectifiers, Wave Shaping, Small Signal Circuits	31																																																																																																			
		<div>November 2008</div> <table><tr><td>S</td><td>M</td><td>T</td><td>W</td><td>Th</td><td>F</td><td>Sa</td></tr><tr><td></td><td></td><td></td><td></td><td></td><td></td><td>1</td></tr><tr><td>2</td><td>3</td><td>4</td><td>5</td><td>6</td><td>7</td><td>8</td></tr><tr><td>9</td><td>10</td><td>11</td><td>12</td><td>13</td><td>14</td><td>15</td></tr><tr><td>16</td><td>17</td><td>18</td><td>19</td><td>20</td><td>21</td><td>22</td></tr><tr><td>23</td><td>24</td><td>25</td><td>26</td><td>27</td><td>28</td><td>29</td></tr><tr><td>30</td><td></td><td></td><td></td><td></td><td></td><td></td></tr></table>	S	M	T	W	Th	F	Sa							1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30							<div>January 2009</div> <table><tr><td>S</td><td>M</td><td>T</td><td>W</td><td>Th</td><td>F</td><td>Sa</td></tr><tr><td></td><td></td><td></td><td></td><td>1</td><td>2</td><td>3</td></tr><tr><td>4</td><td>5</td><td>6</td><td>7</td><td>8</td><td>9</td><td>10</td></tr><tr><td>11</td><td>12</td><td>13</td><td>14</td><td>15</td><td>16</td><td>17</td></tr><tr><td>18</td><td>19</td><td>20</td><td>21</td><td>22</td><td>23</td><td>24</td></tr><tr><td>25</td><td>26</td><td>27</td><td>28</td><td>29</td><td>30</td><td>31</td></tr><tr><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr></table>	S	M	T	W	Th	F	Sa					1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31								Notes:
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JANUARY 2009

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4 LAB FINAL THIS WEEK	5 LAB FINAL	6 CHAPTER 13 13.1—13.2 Bipolar Junction Transistors, Common Emitter Characteristics	7	8 CHAPTER 13 13.4—13.6 NPN transistor, large signal models, DC analysis	9	10																																																																																				
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